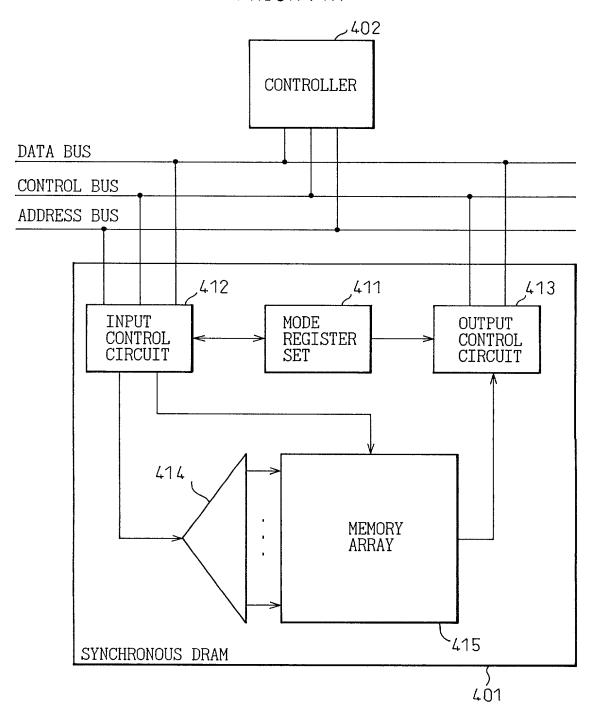
1/9

Fig.1

PRIOR ART



2/9

Fig.2

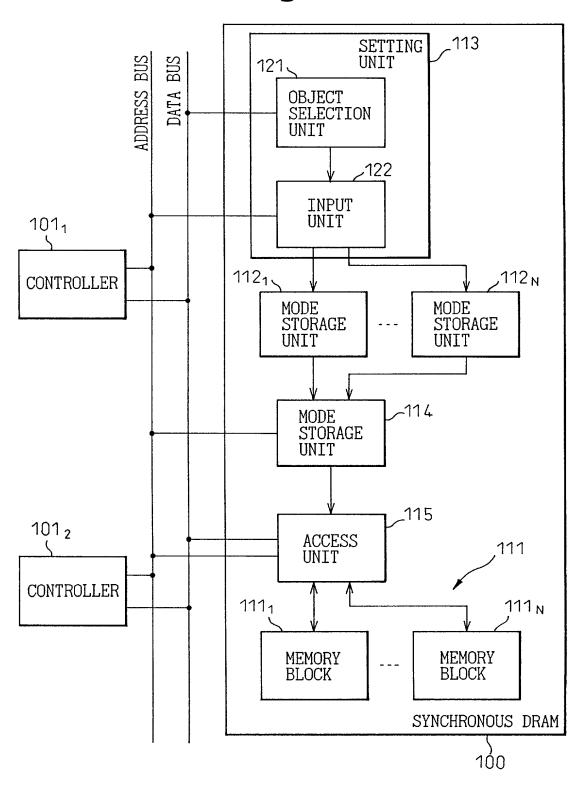




Fig.3

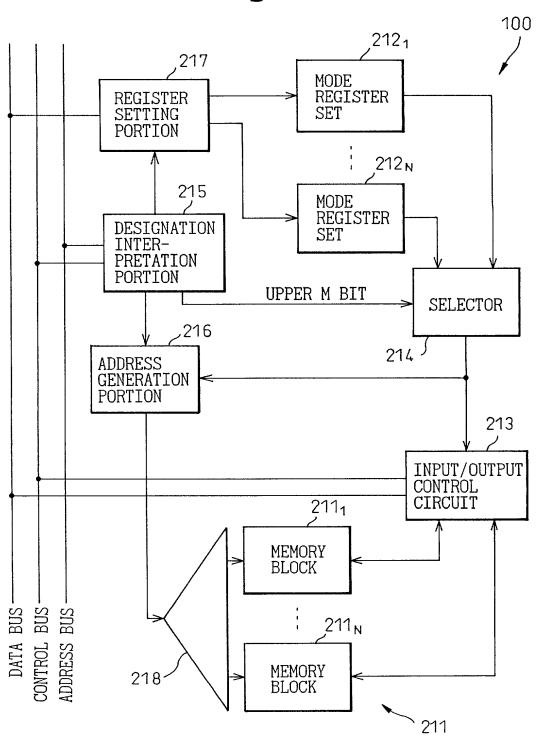
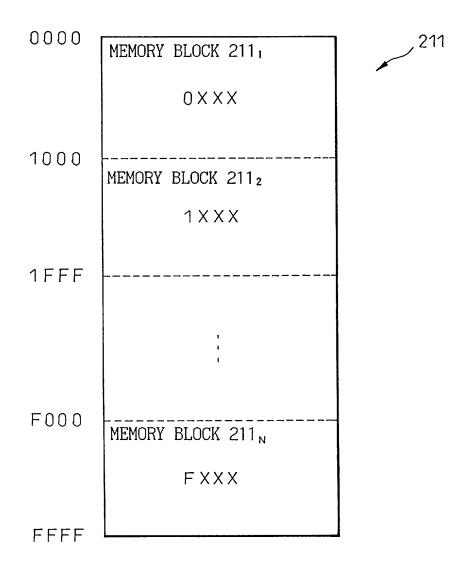


Fig.4



5/9

Fig.5A

Α9	A 8	Α7	Α6	А5	Д4	А3	Α2	Д1	Α0
WBL	TEST	MODE	CAS	LATE	NCY	вт	BUF	RST LE	NGTH

WBL: WRITE BURST LENGTH BT: BURST TYPE

Fig.5D

Fig.5C

TEST MODE

1F21 I	YUDE	
А8	Α7	
0	0	OPERATION MODE SETTING
0	1	SPARE
1	0	SPARE
1	1	SPARE

CAS LATENCY

Α6	Α5	A4	
0	0	0	SPARE
0	0	1	2
0	1	0	3
0	1	1	SPARE
;	i	:	i i
1	1	1	SPARE

Fig.5B

BUST	LENGTH

י וכטם				
Α2	Δ1	Α0	BT = 0	BT = 1
0	0	0	1	1
0	0	1	2	2
0	1	0	4	4
0	1	1	8	8
1	0	0	SPARE	SPARE
i	ł	i	1	i
1	1	1	FULL BURST	SPARE
	A 2 0 0 0	A 2 A 1 0 0 0 0 0 0 1	A2 A1 A0 0 0 0 0 0 1 0 1 0	A2 A1 A0 BT = 0 0 0 0 1 0 0 1 2 0 1 0 4 0 1 1 8 1 0 0 SPARE i i i i

Fig.5E

Α9	WBL				
0	BURST				
1	SINGLE BIT				
Д3	вТ				
0	SEQUENTIAL				
1	INTERLEAVE				

⁶/₉ Fig.6A

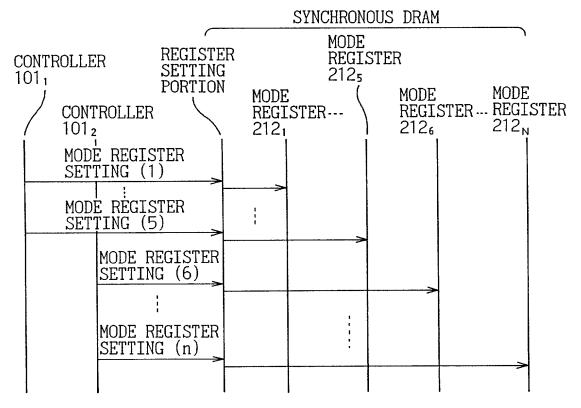


Fig. 6B

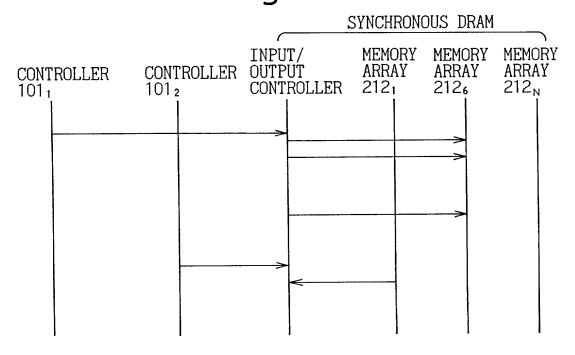
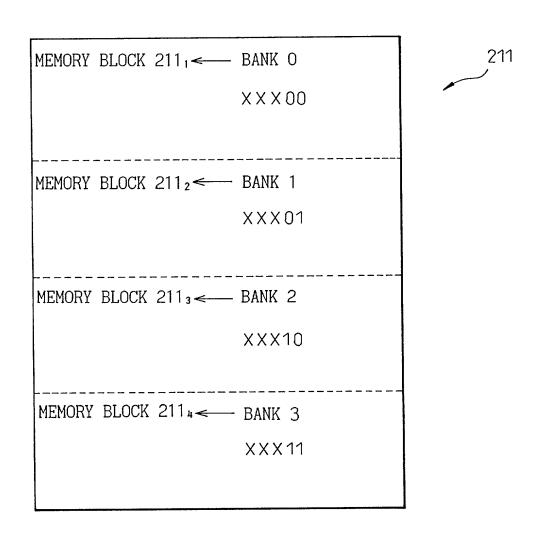
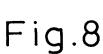


Fig.7



8/9



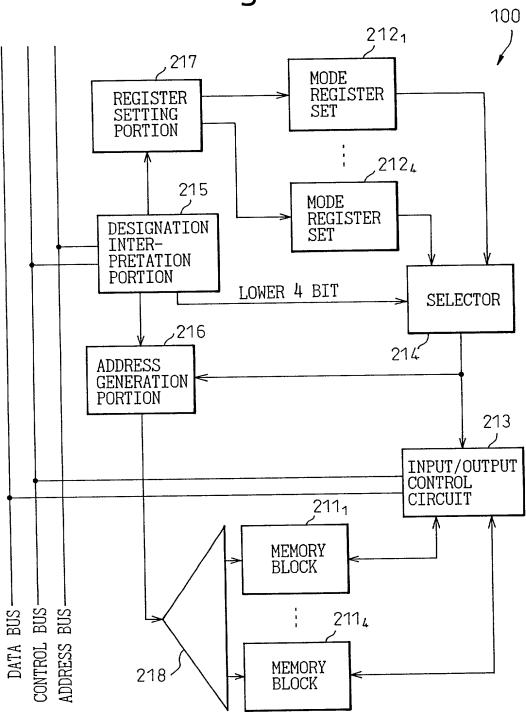


Fig.9

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A9	Α8	Α7	Α6	А5	Д4	А3	Α2	Δ1	Α0
WBL	TEST	MODE	CAS	CAS LATENCY		вт	BURST LENGTH		NGTH
	WBL: WRITE I BT: BURST T								LENGTH
	TEST	r MODE						•	
	Α8	3 Д7							
	0	0	OPE	RATION	MODE	SETTIN	IG (1)		
	0	1	OPE	RATION	MODE	SETTIN	IG (2)		
	1	0	OPE	RATION	MODE	SETTIN	NG (3)		
	1	1	OPE	RATION	MODE	SETTIN	NG (4)		